Fig. 1

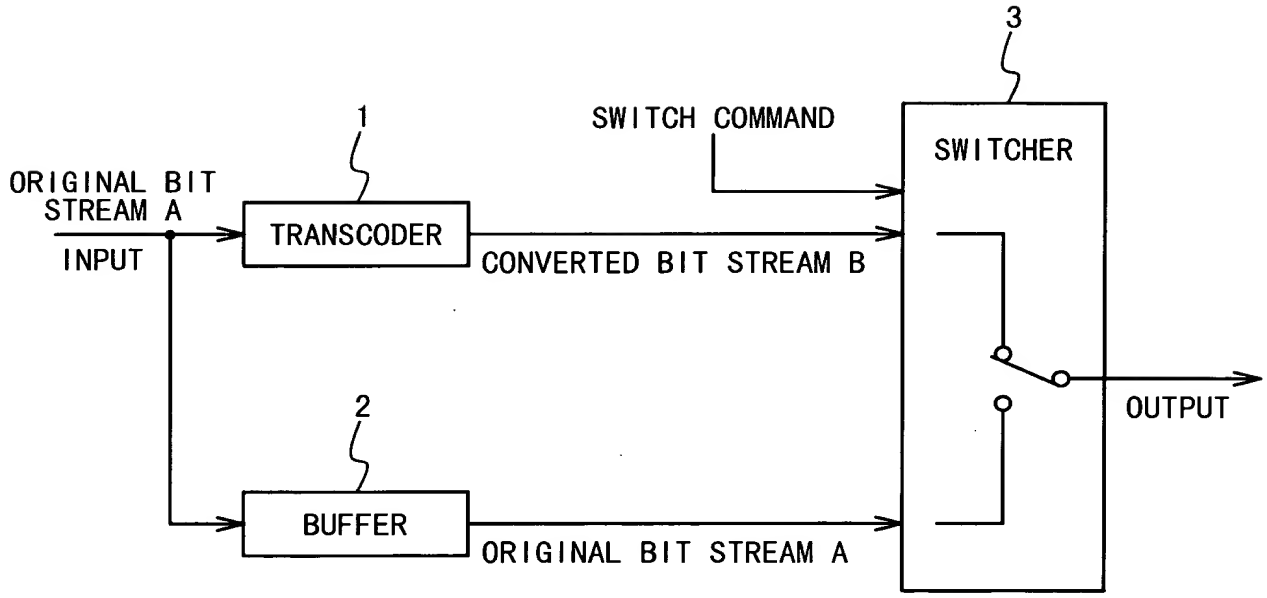


Fig. 2A

Fig. 2B

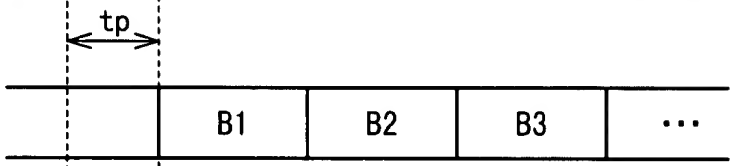


Fig. 2C

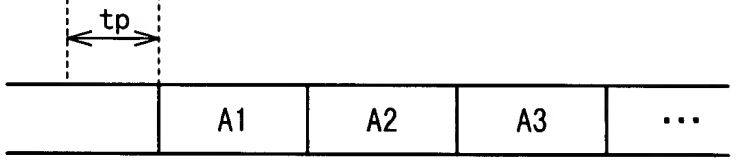


Fig. 3

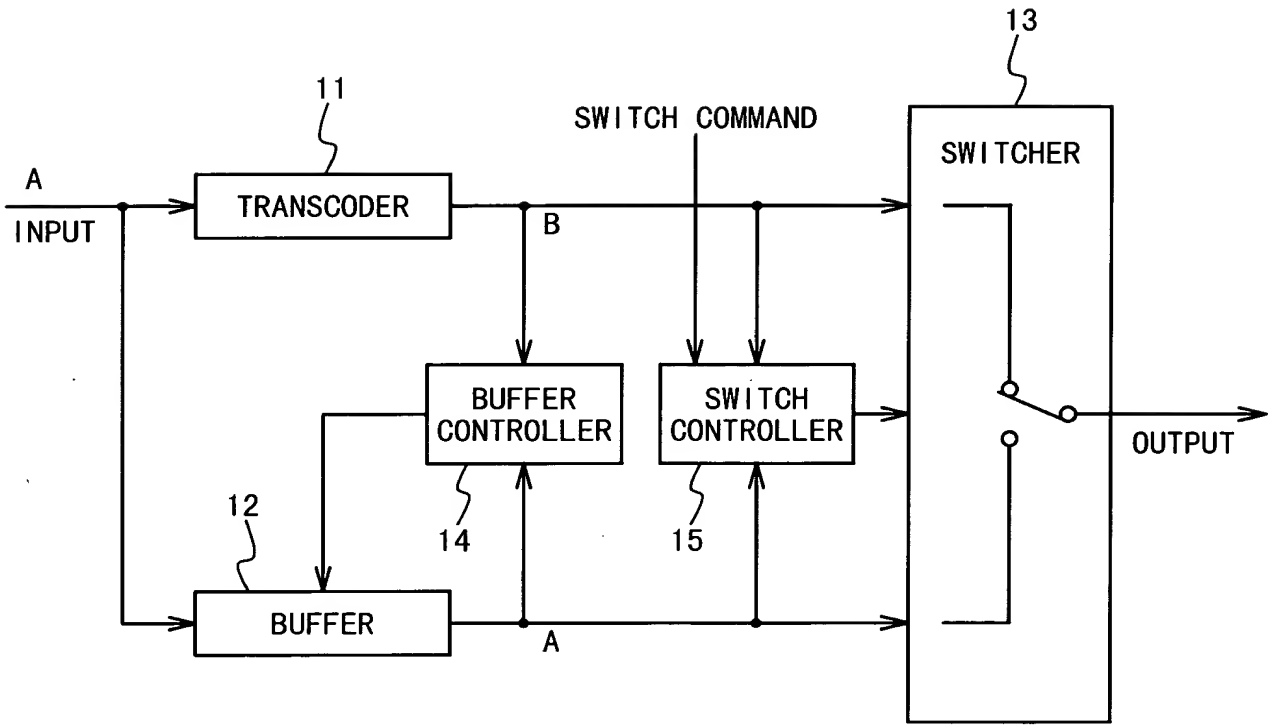
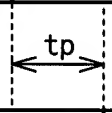


Fig. 4A

INPUTTED ORIGINAL BIT STREAM A

Fig. 4B



CONVERTED BIT STREAM B
OUTPUTTED BY TRANSCODER 11

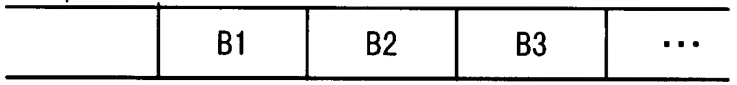


Fig. 4C

BUFFER CONTROLLER 14

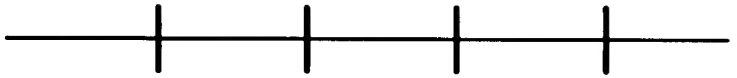


Fig. 4D

ORIGINAL BIT STREAM A
OUTPUTTED BY BUFFER 12

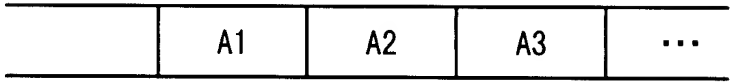


Fig. 5A

ORIGINAL OR CONVERTED BIT STREAM

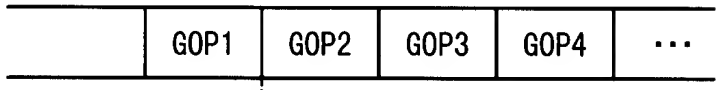


Fig. 5B

SWITCH COMMAND

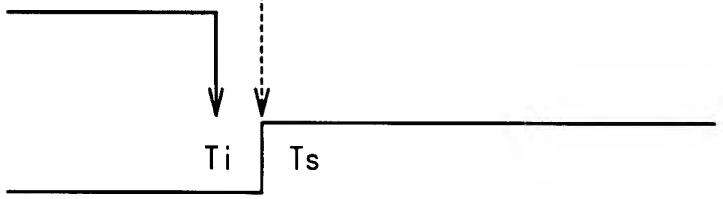


Fig. 5C

SWITCH CONTROLLER 15